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Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	08/902809
Filing Date	July 30, 1997
First Named Inventor	Schuegraf, Klaus
Group Art Unit	2811
Examiner Name	Nadav, Ori

Sheet 1 of 1

Attorney Docket No: 303.278US1

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
ON	US-5,526,306	06/11/1996	Hikawa, Tetsuo, et al.	365	182	06/07/1994
ON	US-5,925,918	07/20/1999	Wu, Z., et al.	257	413	07/30/1997
ON	US-5,929,494	07/27/1999	Li, Che C.	257	390	10/20/1997
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ON	US-6,075,274	06/13/2000	Wu, Z., et al.	257	413	05/28/1999

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
ON		GLASSER, LANCE A., et al., "The Design and Analysis of VLSI Circuits", (1985), pp. 100-103, 176-179, 182-183	
ON		WESTE, NEIL H., "Principles of CMOS VLSI Design - a systems perspective", Addison-Wesley publishing company, 2nd ed., AT&T, (1993), pp. 71-72	

EXAMINER

ON

DATE CONSIDERED

9/16/03

Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional) ² Applicant is to place a check mark here if English language Translation is attached

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